Features

- Supply Voltage: 4.5 V to 40 V
- Offset Voltage: ±30 μV Maximum
- Differential Input Voltage Range to Supply Rail, can Work as Comparator
- Input Rail to $-V_S$, Rail to Rail Output
- Drive Any Capacitive Load
- Bandwidth: 6 MHz, Slew Rate: 5 V/μs
- Excellent EMI Suppress Performance: 85 dB at 1 GHz
- Over-Temperature Protection
- Low Noise: 8 nV/√Hz at 1 kHz
- 2 kV HBM, 1 kV CDM, 500 mA Latch Up
- –40°C to 125°C Operation Temperature Range

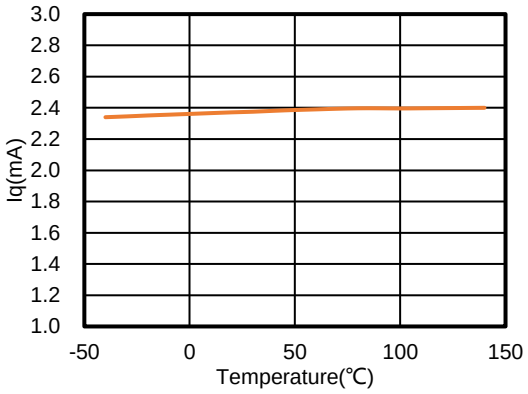
Applications

- Instrumentation
- Active Filters, ASIC Input or Output Amplifier
- Sensor Interface
- Motor Control
- Industrial Control

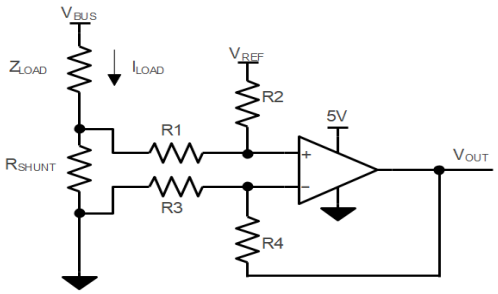
Description

The TPA186X series amplifiers are the newest high supply voltage amplifiers with 30 μV low offset, low noise, and stable high-frequency response. They incorporate 3PEAK's proprietary and patented design techniques to achieve excellent AC performance with 6 MHz bandwidth, 5 V/μs slew rate, and low distortion while drawing only 1.4 mA quiescent current per amplifier. The input common-mode voltage range extends to V–, and the outputs swing rail-to-rail.

The TPA186X has an over-temperature protection feature to guarantee chip safety. The output of TPA186X will enter high impendence when the die temperature reaches around 170°C and will recover the function when the die temperature is down to around 150°C. The product has a very small power temperature coefficient, which is helpful for temperature-sensitive applications.



Typical Application Circuit



$V_{OUT} = (I_{LOAD} \times R_{SHUNT}) \times (R2 / R1) + V_{REF}$

When R3 = R1, R2 = R4, $R_{SHUNT} << R1$

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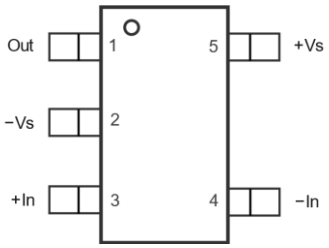
Revision History

Date	Revision	Notes
2019/1/10	Rev.Pre	Pre-Release Version
2019/9/10	Rev.0	Initial Version
2019/11/27	Rev.0.01	Removed Part Number: TPA1862-TSR, Add Part Number: TPA1864-SR, TPA1864-TR
2020/4/26	Rev.A	Added Test Figure
2020/8/1	Rev.A.1	Added More Test Figure
2020/11/6	Rev.A.2	Updated Test Figure: Iq vs. temperature, Vout vs. Iout
2021/5/4	Rev.A.3	Added Tape and Reel Information
2021/7/7	Rev.A.4	Updated maximum rating: Input voltage: ($-V_S$) – 0.3 to ($+V_S$) + 0.3 -> ($-V_S$) – 0.3 to 40 V Differential Input Voltage: ($+V_S$) - ($-V_S$) -> ($-V_S$) - ($+V_S$) to ($+V_S$) - ($-V_S$)
2022/8/18	Rev.A.5	Updated to new document format Updated the working voltage to 40 V, the absolute rating voltage to 42 V Add new package: TPA1862-DF7R

Pin Configuration and Functions

TPA1861
SOT23-5 Package
Top View



TPA1861
SOP8 Package
Top View

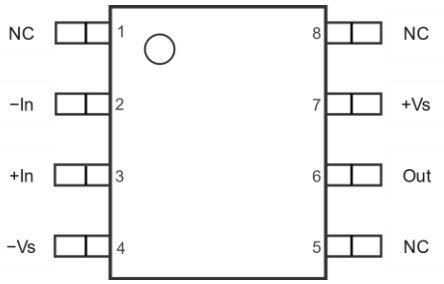
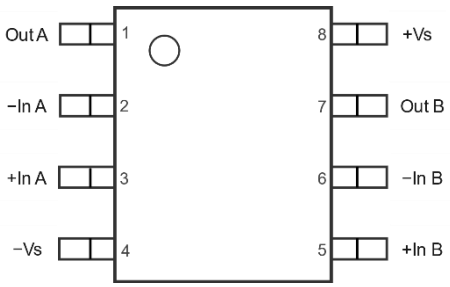


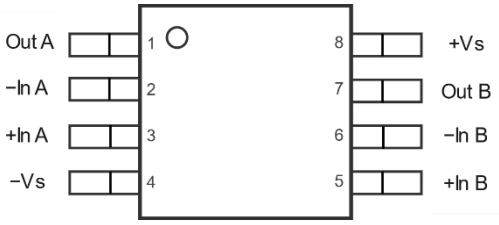
Table 1. Pin Functions: TPA1861

Pin		Name	I/O	Description
SOT23-5	SOP8			
1	6	Out	Output	Output
2	4	$-V_S$		Negative power supply
3	3	+In	Input	Noninverting input
4	2	−In	Input	Inverting input
5	7	$+V_S$		Positive power supply
	1	NC		Not connected
	5	NC		Not connected
	8	NC		Not connected

TPA1862
SOP8 Package
Top View



TPA1862
MSOP8 Package
Top View



TPA1862
DFN3X3-8 Package
Top View

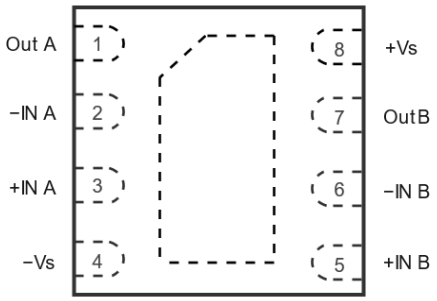
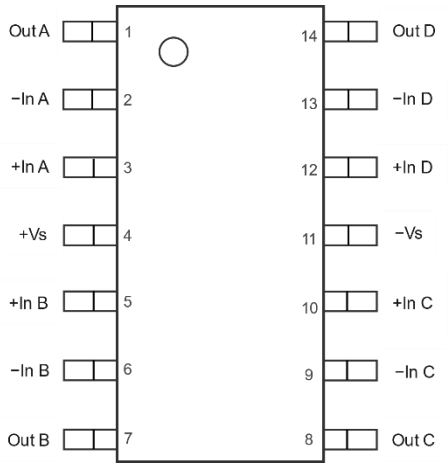


Table 2. Pin Functions: TPA1862

Pin	Name	I/O	Description
1	Out A	Output	Output
2	−In A	Input	Inverting input
3	+In A	Input	Noninverting input
4	$-V_S$		Negative power supply
5	+In B	Input	Noninverting input
6	−In B	Input	Inverting input
7	Out B	Output	Output
8	$+V_S$		Positive power supply

TPA1864
SOP14 Package
Top View



TPA1864
TSSOP14 Package
Top View

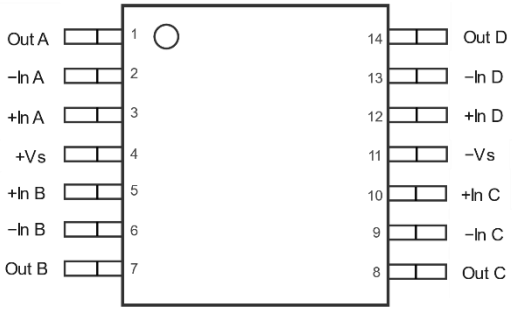


Table 3. Pin Functions: TPA1864

Pin	Name	I/O	Description
1	Out A	Output	Output
2	−In A	Input	Inverting input
3	+In A	Input	Noninverting input
4	$+V_S$		Positive power supply
5	+In B	Input	Noninverting input
6	−In B	Input	Inverting input
7	Out B	Output	Output
8	Out C	Output	Output
9	−In C	Input	Inverting input
10	+In C	Input	Noninverting input
11	$-V_S$		Negative power supply
12	+In D	Input	Noninverting input
13	−In D	Input	Inverting input
14	Out D	Output	Output

Specifications

Absolute Maximum Ratings

Over operating ambient temperature (unless otherwise noted) (1)

Parameter	Min	Max	Unit
Supply Voltage, ($+V_S$) – ($-V_S$)		42 V	V
Input Voltage	($-V_S$) – 0.3	42 V	V
Differential Input Voltage	($-V_S$) − ($+V_S$)	($+V_S$) − ($-V_S$)	V
Input Current: +IN, –IN (2)	−10	10	mA
Output Voltage	($-V_S$) − 0.3	($+V_S$) + 0.3	V
Output Short-Circuit Duration (3)		Infinite	
Maximum Operating Junction Temperature		150	°C
Operating Temperature Range	−40	125	°C
Storage Temperature Range	−65	150	°C
Lead Temperature (Soldering, 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.
(2) The inputs are protected by ESD protection diodes to the negative power supply. If the input extends more than 300 mV beyond the negative power supply, the input current should be limited to less than 10 mA.
(3) A heat sink may be required to keep the junction temperature below the absolute maximum. This depends on the power supply voltage and how many amplifiers are shorted. Thermal resistance varies with the amount of PC board metal connected to the package. The specified values are for short traces connected to the leads.

ESD, Electrostatic Discharge Protection

Symbol	Parameter	Condition	Minimum Level	Unit
HBM	Human Body Model ESD(1)	ANSI/ESDA/JEDEC JS-001	2	kV
CDM	Charged Device Model ESD(2)	ANSI/ESDA/JEDEC JS-002	1	kV
LU	Latch Up	JESD 78, 25°C	500	mA
		JESD 78, 125°C	250	mA

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

Recommended Operating Conditions

Parameter		Min	Typ	Max	Unit
V_S	Supply Voltage, ($+V_S$) – ($-V_S$)	4.5		40	V
T_A	Operating Temperature Range	–40		125	°C

Thermal Information

Package Type	θ_{JA}	θ_{JC}	Unit
SOT23-5	250	81	°C/W
SOP8	158	43	°C/W
MSOP8	210	45	°C/W
SOP14	120	36	°C/W
TSSOP14	180	35	°C/W

Electrical Characteristics

All test condition is V_S = 30 V, T_A = 25°C, R_L = 10 kΩ, unless otherwise noted.

Symbol	Parameter	Conditions	T_A	Min	Typ	Max	Unit
Power Supply							
V_S	Supply Voltage Range			4.5		40	V
I_Q	Quiescent Current per Amplifier	V_S = 40 V			1.6	2	mA
		V_S = 30 V			1.4	1.6	mA
			-40°C to 125°C			1.8	mA
		V_S = 5 V			1.2	1.5	mA
			-40°C to 125°C			1.7	mA
PSRR	Power Supply Rejection Ratio	V_S = 4.5 V to 36 V		125	140		dB
			-40°C to 125°C	120			dB
Input Characteristics							
V_{OS}	Input Offset Voltage	V_S = 40 V, V_{CM} = 20 V		-30		30	μV
		V_S = 30 V, V_{CM} = 15 V		-30		30	μV
			-40°C to 125°C	-50		50	μV
		V_S = 5 V, V_{CM} = 2.5 V		-30		30	μV
			-40°C to 125°C	-50		50	μV
V_{OS} TC	Input Offset Voltage Drift		-40°C to 125°C		0.01	0.2	μV/°C
I_B	Input Bias Current				100		pA
			-40°C to 125°C		100		pA
I_{OS}	Input Offset Current				100		pA
I_{IN}	Different Input Current	V_S = 36 V, V_{ID} = 36 V			10	100	μA
			-40°C to 125°C			120	μA
C_{IN}	Input Capacitance	Differential Mode			5		pF
		Common Mode			2.5		pF
Av	Open-loop Voltage Gain	R_{LOAD} = 10 kΩ, VOUT = 0.5 V to 29.5 V		130	140		dB
			-40°C to 125°C	125			dB
V_{CMR}	Common-mode Input Voltage Range			(V-)		(V+) – 1.5	V
CMRR	Common Mode Rejection Ratio	V_{CM} = 0 V to 28.5 V		125	140		dB
			-40°C to 125°C	120			dB

Electrical Characteristics (Continued)

All test condition is V_S = 30 V, T_A = 25°C, R_L = 10 kΩ, unless otherwise noted.

Symbol	Parameter	Conditions	T_A	Min	Typ	Max	Unit
Output Characteristics							
	Output Swing from Positive Rail	R_{LOAD} = 100 kΩ to V_S/2			10	15	mV
			-40°C to 125°C			30	mV
		R_{LOAD} = 10 kΩ to V_S/2			75	100	mV
			-40°C to 125°C			180	mV
		R_{LOAD} = 2 kΩ to V_S/2			400	500	mV
			-40°C to 125°C			750	mV
	Output Swing from Negative Rail	R_{LOAD} = 100 kΩ to V_S/2			3	5	mV
			-40°C to 125°C			10	mV
		R_{LOAD} = 10 kΩ to V_S/2			25	35	mV
			-40°C to 125°C			60	mV
		R_{LOAD} = 2 kΩ to V_S/2			130	150	mV
			-40°C to 125°C			300	mV
I_{SC}	Output Short-Circuit Current	Source		60	95		mA
			-40°C to 85°C	40			mA
			-40°C to 125°C	35			mA
		Sink		130	150		mA
			-40°C to 85°C	100			mA
			-40°C to 125°C	85			mA
	Capacitive Load Drive				1		nF
AC Specifications							
GBW	Gain-Bandwidth Product				6		MHz
SR	Slew Rate	G = 1, 10 V step		3	5		V/μs
			-40°C to 125°C	2.2			V/μs
t_{OR}	Overload Recovery				500		ns
t_S	Settling Time, 0.1%	G = 1, 10 V step			7		μs
	Settling Time, 0.01%				12		μs
PM	Phase Margin	R_L=10 K, C_L=100 pF			70		°
GM	Gain Margin	R_L=10 K, C_L=100 pF			15		dB

Electrical Characteristics (Continued)

All test condition is V_S = 30 V, T_A = 25°C, R_L = 10 kΩ, unless otherwise noted.

Symbol	Parameter	Conditions	T_A	Min	Typ	Max	Unit
Noise Performance							
E_N	Input Voltage Noise	f = 0.1 Hz to 10 Hz			0.1		μV_{PP}
e_N	Input Voltage Noise Density	f = 0.1 Hz			8		nV/√Hz
		f = 1 kHz			8		nV/√Hz
		f = 10 kHz			10		nV/√Hz
		f = 100 kHz			20		nV/√Hz
i_N	Input Current Noise	f = 10 kHz			200		fA/√Hz
THD+N	Total Harmonic Distortion and Noise	f = 1 kHz, G =1, R_L = 10 kΩ, V_{OUT} = 6 V_{RMS}			0.0005		%

Typical Performance Characteristics

Test condition: V_S = ±15 V, V_{CM} = 0 V, R_L = 10 kΩ, unless otherwise specified.

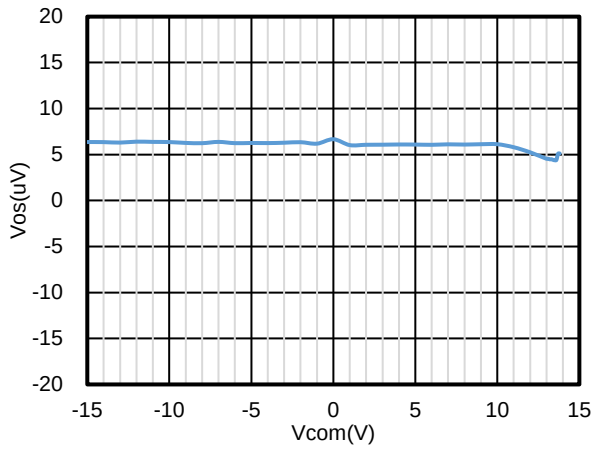


Figure 1. Offset Voltage vs. Common-Mode Voltage

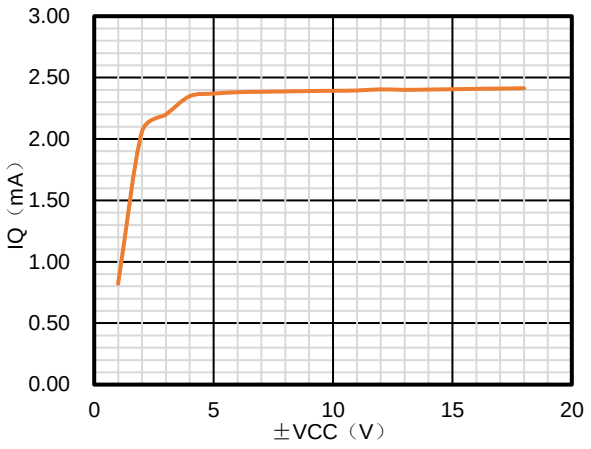


Figure 2. Iq vs. Supply Voltage

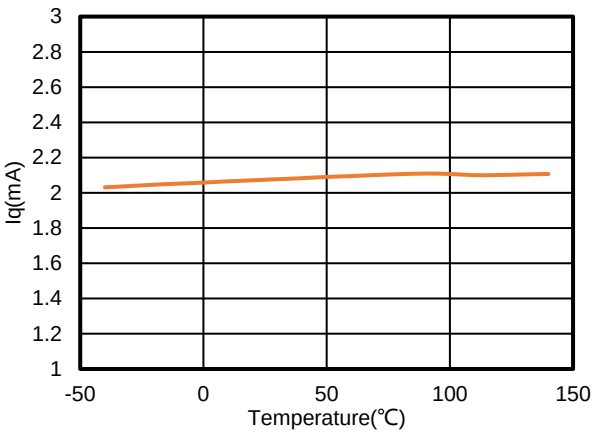


Figure 3. Iq vs. Temperature, +-2.5 V Supply, TPA1862

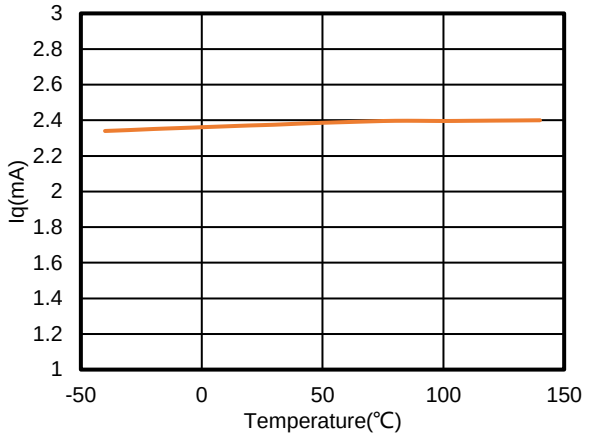


Figure 4. Iq vs. Temperature, +-15 V Supply, TPA1862

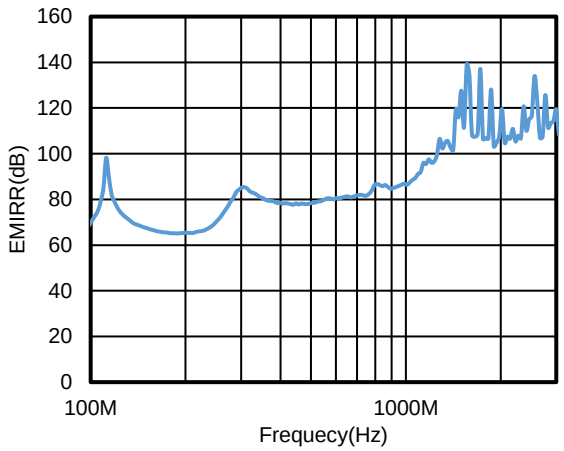


Figure 5. EMIRR vs. Frequency

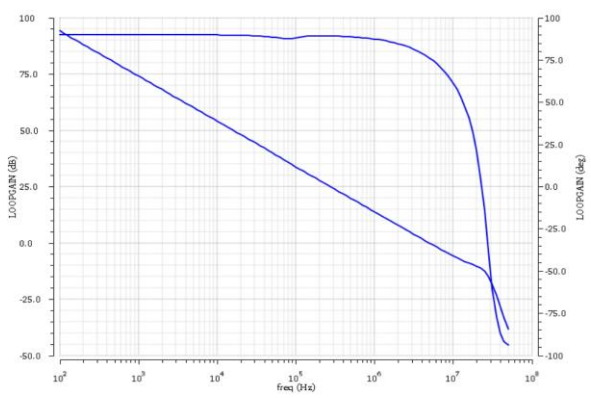


Figure 6. Open Loop Gain and Phase vs. Frequency

R_L = 10 kΩ, C_L = 50 pF

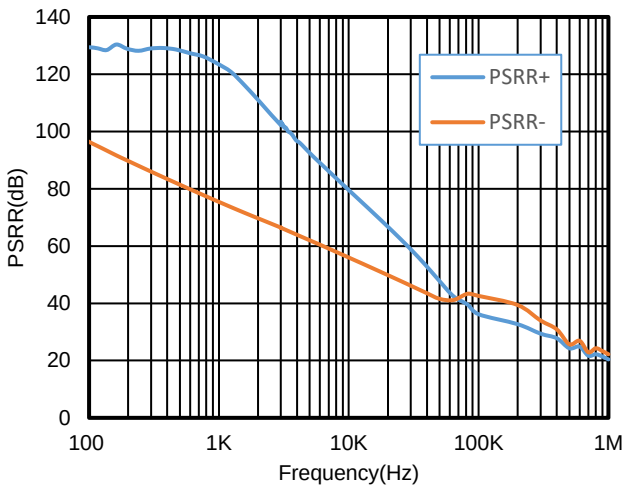


Figure 7. PSRR vs. Frequency

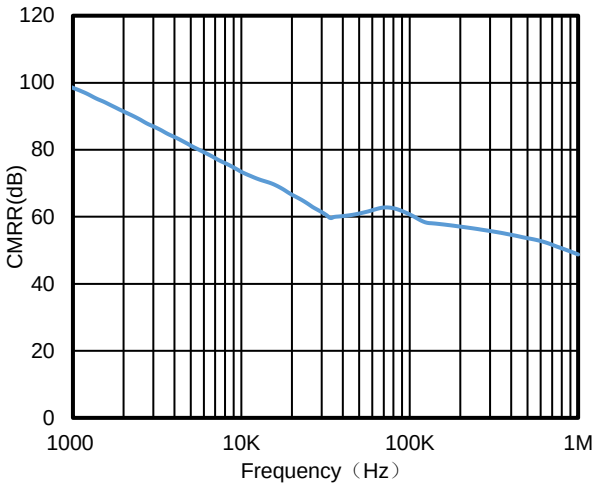
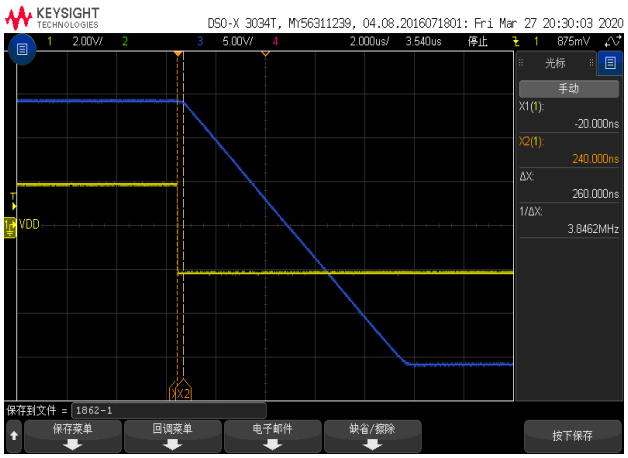


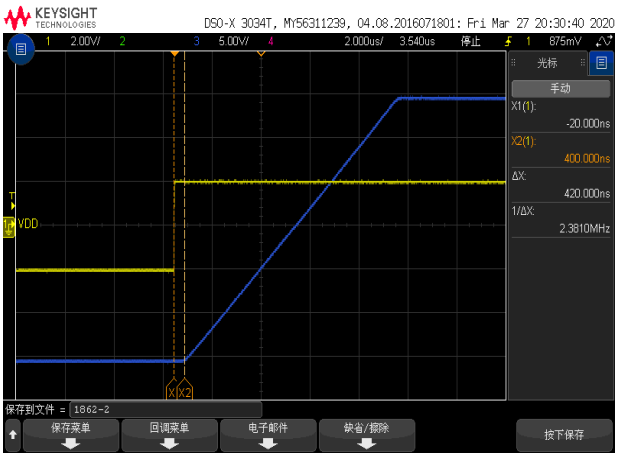
Figure 8. CMRR vs. Frequency



Time: 2 us/div, Measure Time: 260 ns

R_L=2 K, C_L=100 pF, G=10

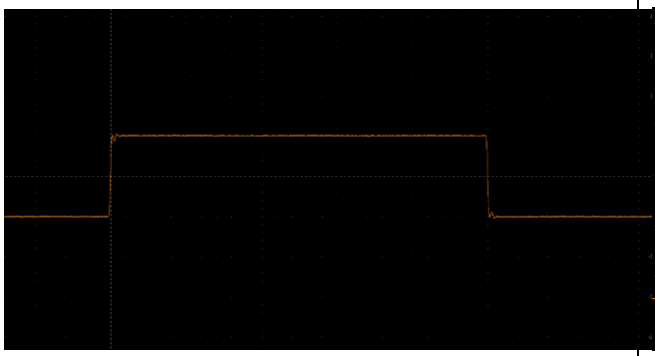
Figure 9. Positive Overload Recovery



Time: 2 us/div, Measure Time: 420 ns

R_L=2 K, C_L=100 pF, G=10

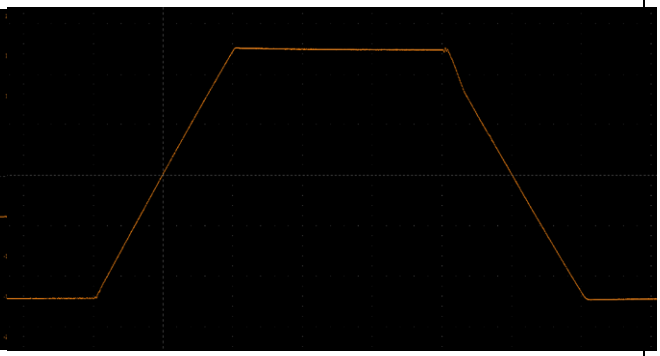
Figure 10. Negative Overload Recovery



Voltage: 50 mV/div, Time: 2 us/div

R_L=2 K, C_L=100 pF, G=1

Figure 11. 100 mV Signal Step Response



Voltage: 2 V/div, Time: 2 μs/div

R_L=2 K, C_L=100 pF, G=1

Figure 12. 10 V Signal Step Response

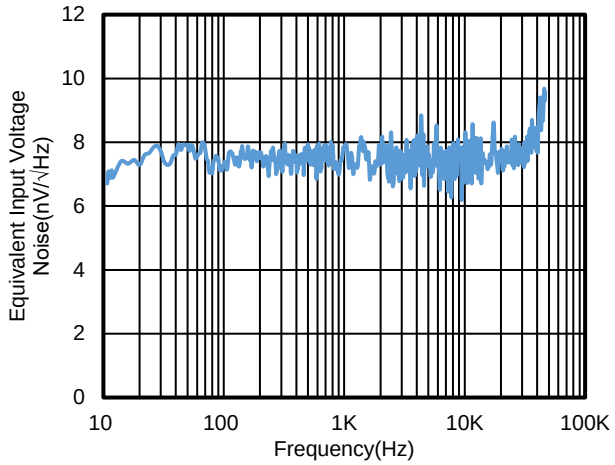


Figure 13. Voltage Noise Density vs. Frequency

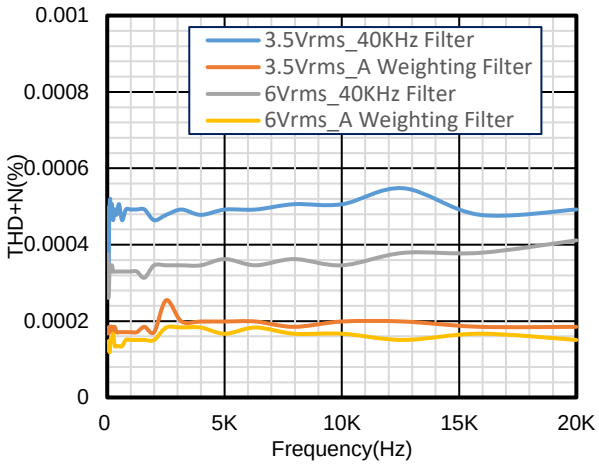


Figure 14. THD vs. Frequency, G = 1

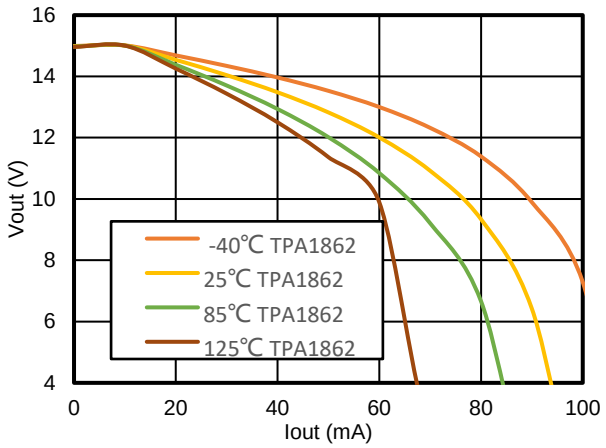


Figure 15. V_{OUT} vs. I_{OUT}, Source

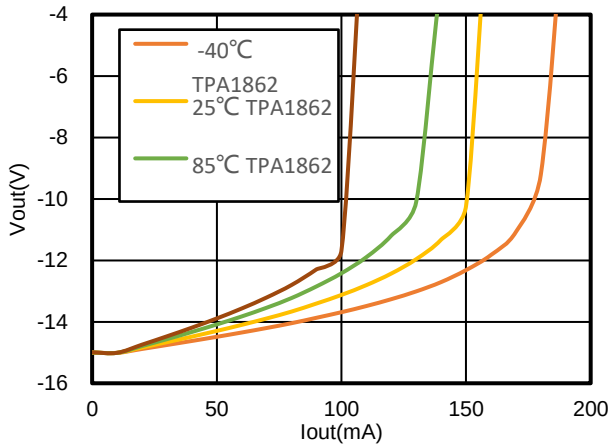


Figure 16. V_{OUT} vs. I_{OUT}, Sink

Detailed Description

Overview

The TPA186x series op amps can operate on a single-supply voltage (4.5 V to 40 V), or a split-supply voltage (±2.25 V to ±20 V), making them highly versatile and easy to use. The power-supply pins should have local bypass ceramic capacitors (typically 0.01 µF to 0.1 µF). Parameters that can exhibit variance with regard to operating voltage or temperature are presented in the Typical Characteristics.

Functional Block Diagram

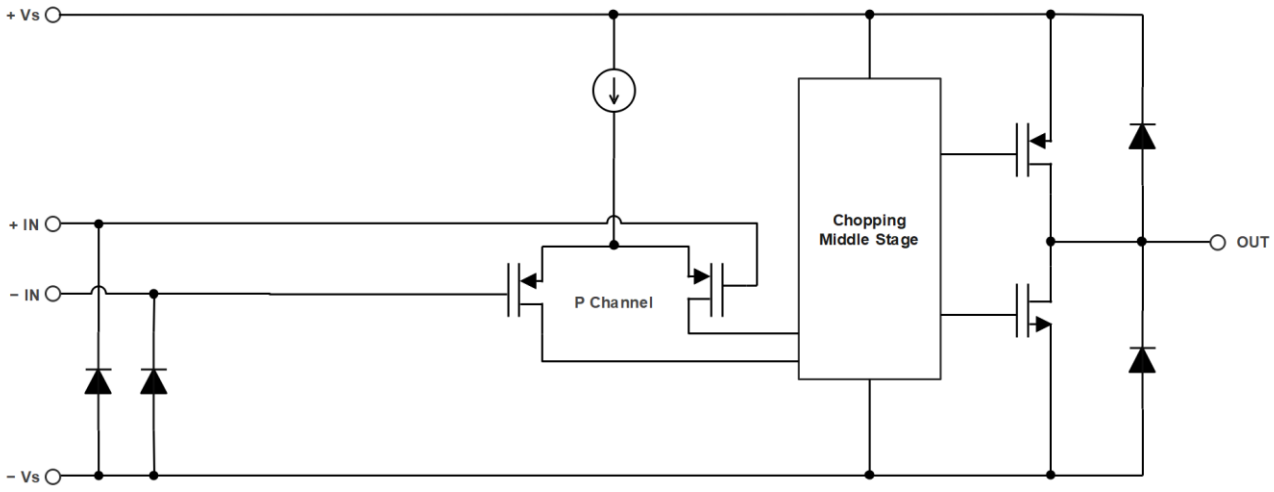


Figure 17. Functional Block Diagram

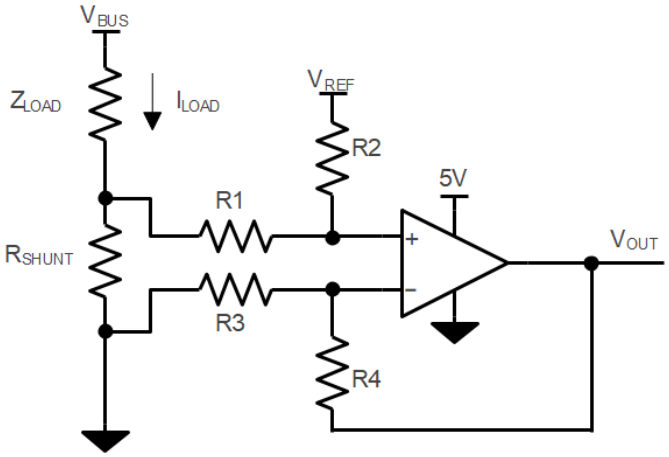
Application and Implementation

NOTE

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Low Side Current Sensing Application

Figure 18 shows the TPA186X configured in a low-side current sensing application. The low-side current sensing method consists of placing a sense resistor between the load and the circuit ground. The voltage dropping across the resistor is amplified by different amplifier circuits with TPA186X. The V_{REF} can be used to add bias voltage to output voltage. Particular attention must be paid to the matching and precision of R1, R2, R3, and R4, to maximize the accuracy of the measurement.



$$V_{OUT} = (I_{LOAD} \times R_{SHUNT}) \times (R2 / R1) + V_{REF}$$

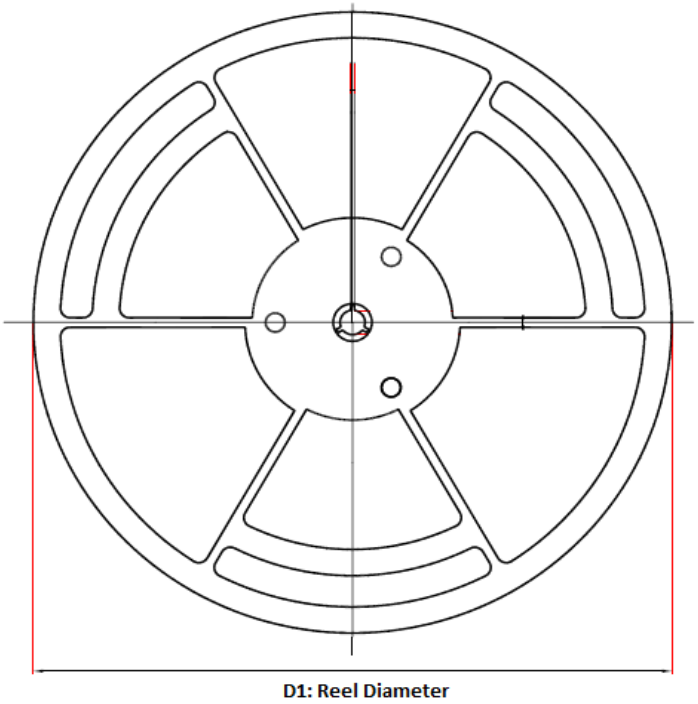
When R3 = R1, R2 = R4, R_{SHUNT} << R1

Figure 18. Dual Supply Operation Connections

Power Supply Recommendations

Place 0.1-μF bypass capacitors close to the power supply pins for reducing coupling errors from the noisy or high impedance power supplies.

Tape and Reel Information

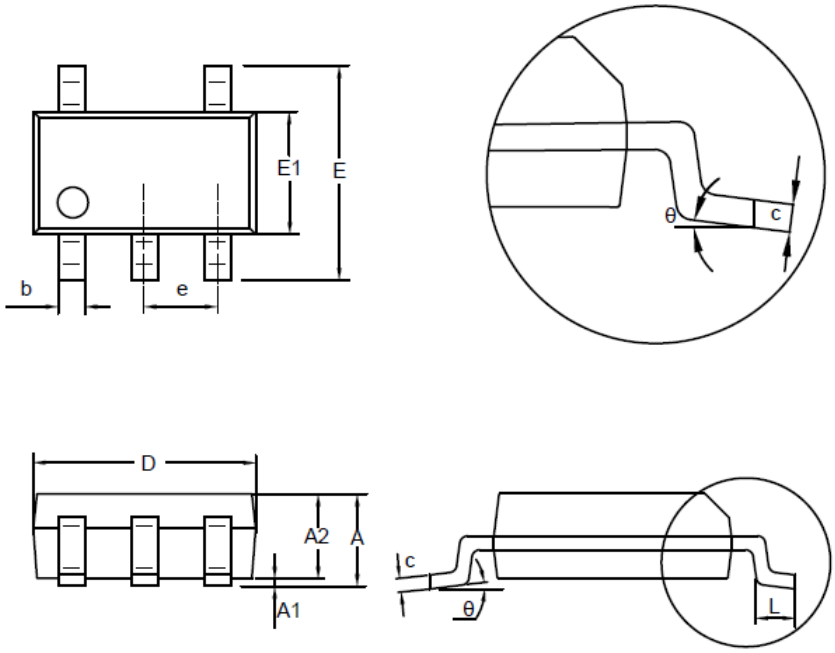


Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPA1861-TR	SOT23-5	180.0	13.1	3.2	3.2	1.4	4.0	8.0	Q3
TPA1861-SR	SOP8	330.0	17.6	6.4	5.4	2.1	8.0	12.0	Q1
TPA1862-SR	SOP8	330.0	17.6	6.4	5.4	2.1	8.0	12.0	Q1
TPA1862-VR	MSOP8	330.0	17.6	5.2	3.3	1.5	8.0	12.0	Q1
TPA1862-DF7R	DFN3X3-8	330.0	17.6	3.4	3.4	1.1	8.0	12.0	Q2
TPA1864-SR	SOP14	330.0	21.6	6.5	9.0	2.1	8.0	16.0	Q1
TPA1864-TR	TSSOP14	330.0	17.6	6.8	5.4	1.2	8.0	12.0	Q1

Package Outline Dimensions

SOT23-5

Package Outline Dimensions S5T(SOT23-5-A)



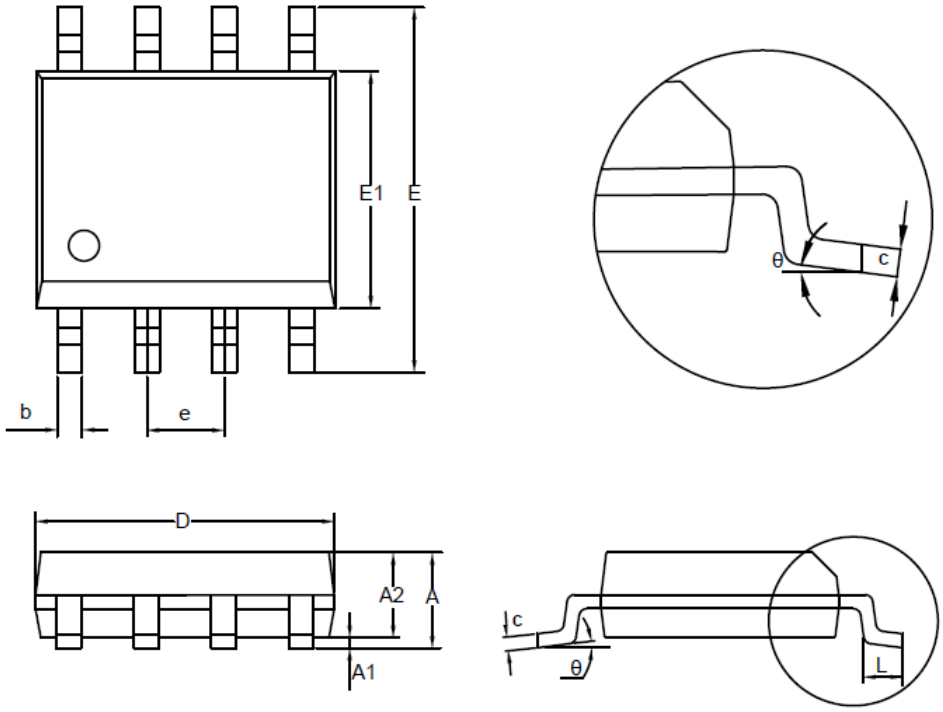
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.150	0.000	0.006
A2	1.000	1.200	0.039	0.047
b	0.280	0.500	0.011	0.020
c	0.100	0.230	0.004	0.009
D	2.820	3.020	0.111	0.119
E	2.600	3.000	0.102	0.118
E1	1.500	1.720	0.059	0.068
e	0.950 BSC		0.037 BSC	
L	0.300	0.600	0.012	0.024
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

SOP8

Package Outline Dimensions SO1(SOP-8-A)



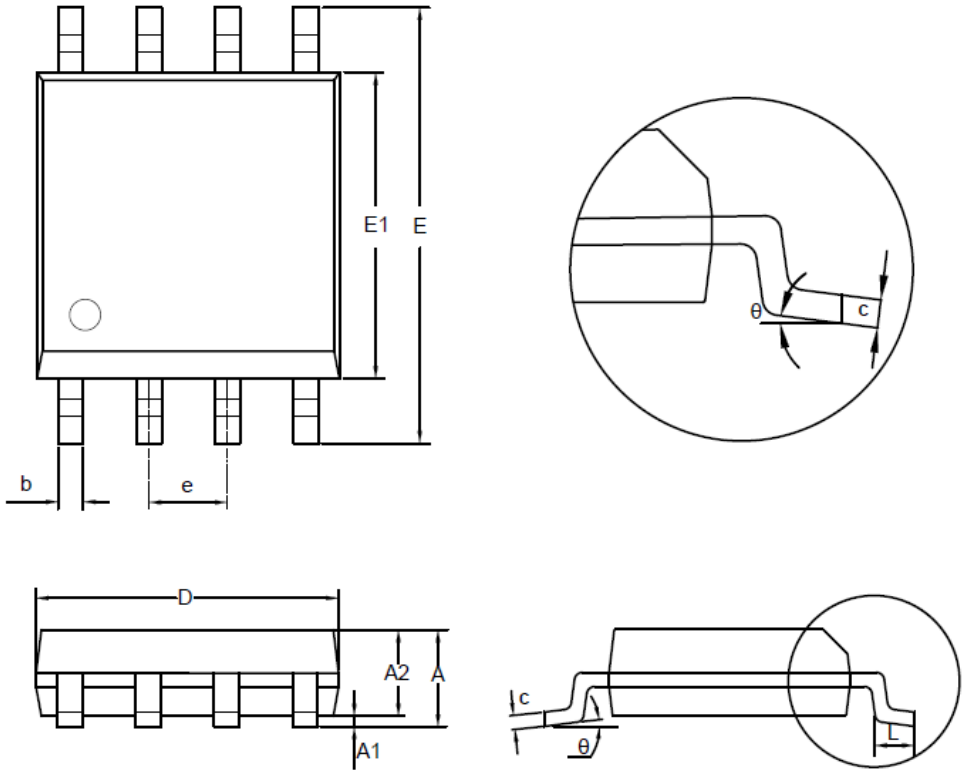
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.550	0.049	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270 BSC		0.050 BSC	
L	0.400	1.000	0.016	0.039
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

MSOP8

Package Outline Dimensions VS1(MSOP-8-A)

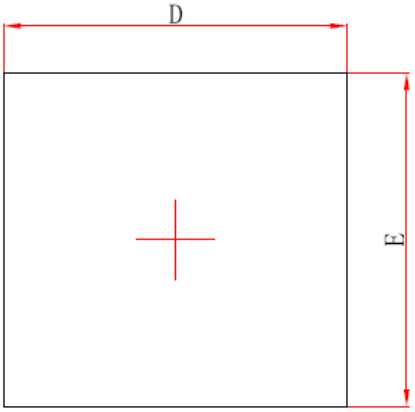


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.800	1.100	0.031	0.043
A1	0.050	0.150	0.002	0.006
A2	0.750	0.950	0.030	0.037
b	0.250	0.380	0.010	0.015
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
E	4.700	5.100	0.185	0.201
E1	2.900	3.100	0.114	0.122
e	0.650 BSC		0.026 BSC	
L	0.400	0.800	0.016	0.031
θ	0	8°	0	8°

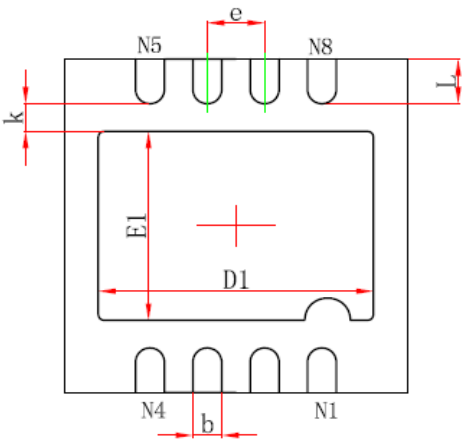
NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

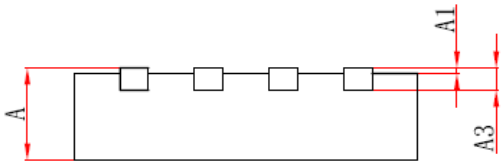
DFN3X3-8



Top View



Bottom View

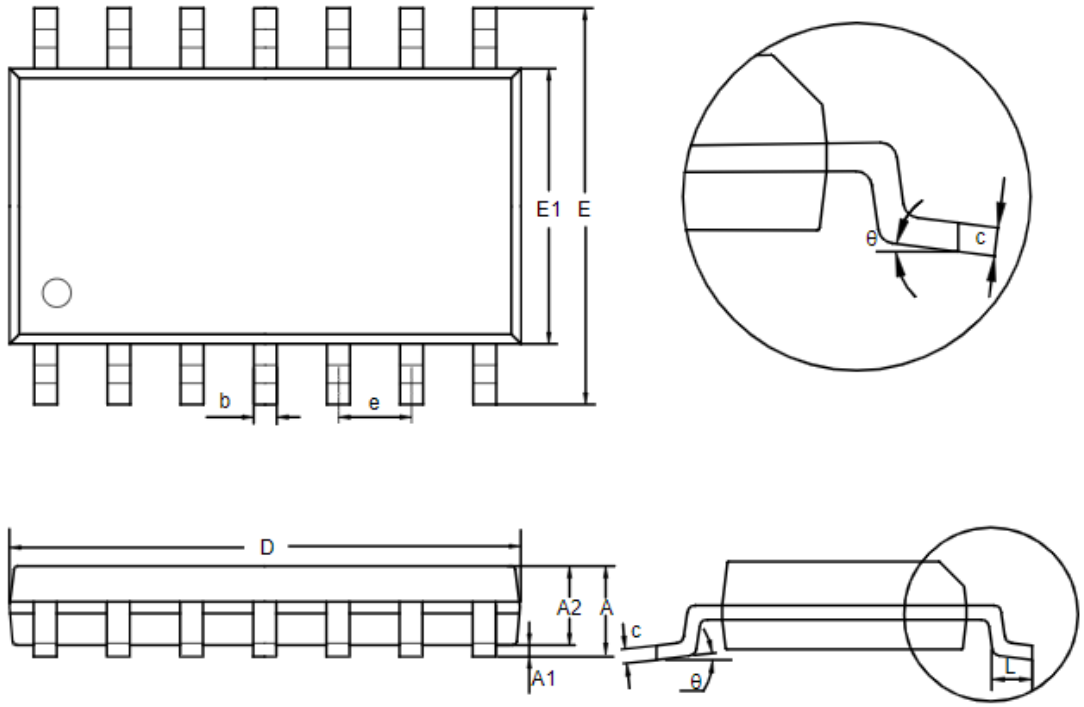


Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	2.924	3.076	0.115	0.121
E	2.924	3.076	0.115	0.121
D1	2.300	2.500	0.091	0.098
E1	1.600	1.800	0.063	0.071
k	0.200MIN.		0.008MIN.	
b	0.200	0.300	0.008	0.012
e	0.500TYP.		0.020TYP.	
L	0.324	0.476	0.013	0.019

SOP14

Package Outline Dimensions SO2(SOP-14-A)



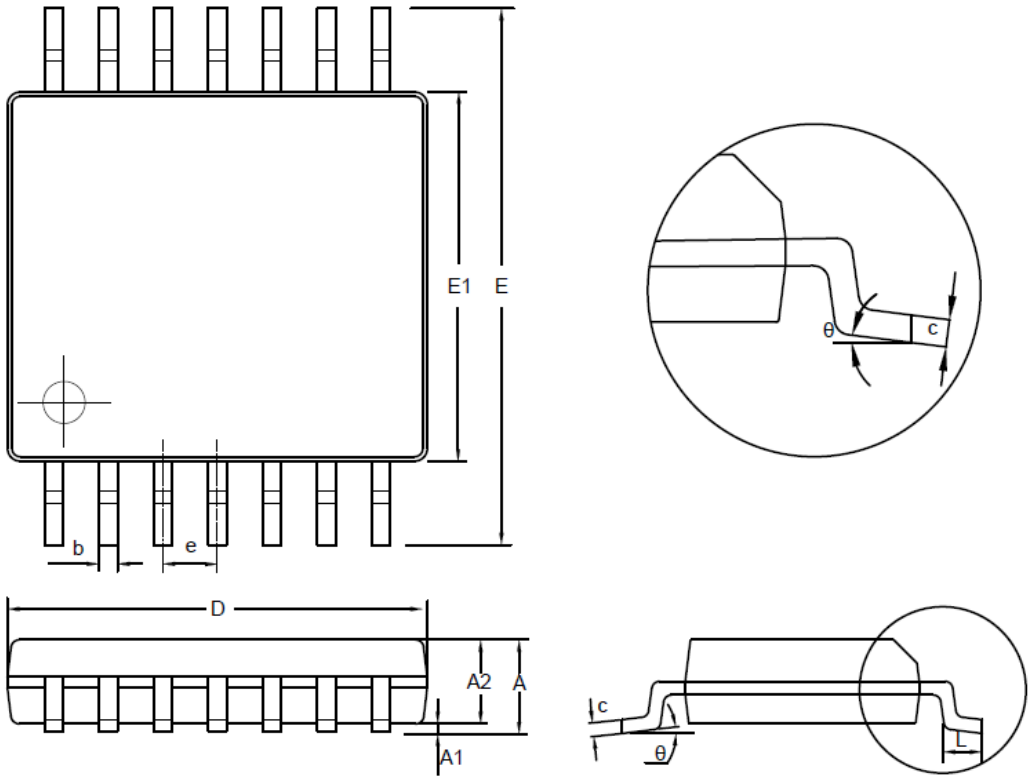
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.650	0.049	0.065
b	0.310	0.510	0.012	0.020
c	0.100	0.250	0.004	0.010
D	8.450	8.850	0.333	0.348
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270 BSC		0.050 BSC	
L	0.400	1.270	0.016	0.050
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

TSSOP14

Package Outline Dimensions TS2(TSSOP-14-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.900	1.200	0.035	0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
D	4.900	5.100	0.193	0.201
E	6.200	6.600	0.244	0.260
E1	4.300	4.500	0.169	0.177
e	0.650 BSC		0.026 BSC	
L	0.450	0.750	0.018	0.030
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPA1861-SR	−40 to 125°C	SOP8	1861	3	Tape and Reel, 4000	Green
TPA1861-TR	−40 to 125°C	SOT23-5	A86	3	Tape and Reel, 3000	Green
TPA1862-SR	−40 to 125°C	SOP8	1862	3	Tape and Reel, 4000	Green
TPA1862-VR	−40 to 125°C	MSOP8	1862	3	Tape and Reel, 3000	Green
TPA1862-DF7R	−40 to 125°C	DFN3X3-8	A1862	3	Tape and Reel, 4000	Green
TPA1864-SR	−40 to 125°C	SOP14	1864	3	Tape and Reel, 2500	Green
TPA1864-TR	−40 to 125°C	TSSOP14	1864	3	Tape and Reel, 3000	Green

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.