

Reliability Data Sheet

Description

The reliability data shown includes Avago Technologies reliability test data from the reliability qualification done on this product family. All of these products use the same LEDs, similar IC, and the same packaging materials, processes, stress conditions and testing. The data in Table 1 and Table 2 reflect actual test data for devices on a per channel basis. Before stress, all devices are preconditioned using a solder reflow process (260 °C peak temp) and 20 temperature cycles (-55 °C to +125 °C, 15 mins dwell, <5 mins transfer). These data are taken from testing on Avago Technologies devices using internal Agilent Technologies process, material specifications, design standards, and statistical process controls. **THEY ARE NOT TRANSFERABLE TO OTHER MANUFACTURERS' SIMILAR PART TYPES.**

Operating Life Test

For valid system reliability calculations it is necessary to adjust for the time when the system is not in operation. Note that if you are using MIL-HDBK-217 for predicting component reliability, the results may not be comparable to those given in Table 2 due to different conditions and factors that have been accounted for in MIL-HDBK-217. For example it is unlikely that your application will exercise all available channels at full rated power with the LED(s) always ON as Avago Technologies testing does. Thus, your application total power and duty cycle must be carefully considered when comparing Table 2 to predictions using MIL-HDBK-217.

Definition of Failure

Inability to switch, i.e. “functional failure” is the definition of failure in this data sheet. Specifically, failure occurs when the device fails to switch ON with 2 times the minimum recommended drive current (but not exceeding the max rating) or fails to switch off when there is no input current

Failure Rate Projections

The demonstrated point mean time to failure (MTTF) is measured at the absolute maximum stress condition. The failure rate projections in Table 2 uses the Arrhenius acceleration relationship, where a 0.43 eV activation energy is used as in the hybrid section of MIL-HDBK-217.

Application Information

The data of Table 1 and 2 were obtained on devices with high temperature operating life duration. An exponential (random) failure distribution is assumed, expressed in units of FIT (failures per billion device hours) are only defined in the random failure portion of the reliability curve.

Table 1. Demonstrated Operating Life Test Performance

Stress Test Condition	Total Device Tested	Total Device Hours	Number of Failed Units	Demonstrated MTTF(hr) @ Ta = +100 °C	Demonstrated FITs @ Ta = +100 °C
Ta = 100 °C, Vcc = 35.0V Iin = 25mA	140	140,000	0	> 140,000	< 7,143

Table 2. Reliability Projection for Device Listed in Title

Ambient Temperature (°C)	Junction Temperature (°C)	Typical (60% Confidence)		90% Confidence	
		MTTF (Hr/fail)	FITs (Fail/10 ⁹ h)	MTTF (Hr/fail)	FITs (Fail/10 ⁹ h)
100	115	152,790	6,545	60,801	16,447
90	105	214,604	4,660	85,399	11,710
80	95	307,043	3,257	122,185	8,184
70	85	448,179	2,231	178,348	5,607
60	75	668,565	1,496	266,049	3,759
50	65	1,021,205	979	406,378	2,461
40	55	1,600,664	625	636,968	1,570
30	45	2,580,854	387	1,027,025	974
25	40	3,314,867	302	1,319,118	758

Table 3. Mechanical Tests (Testing done on a constructional basis)

Test Name	Reference Standard	Test Conditions	Units Tested	Units Failed
Temp Cycling	JA104	-55 to 125 °C Transfer = <5 mins, Dwell = 15 mins 1000 cycles	200	0
Physical Dimensions	JB100	Conformance to datasheet package drawings	20	0
Solderability	JB102	8hrs steam aging (93 °C), followed by solder dip (245 °C,5sec)	20	0
Solderability (Pb-free condition)	NA	8hrs steam aging (93 °C), followed by solder dip (260 °C,5sec)	20	0
Preconditioning	J-STD-020 JA113	As per reference standard (to conform to MSL 1)	180	0

Table 4. Environmental Testing

Test Name	Reference Standard	Test Conditions	Units Tested	Units Failed
Unbiased Autoclave	JA102	Ta = 121°C, RH = 100%, 15psig Time = 168 hours	80	0
High Temperature Storage	JA103	Ta = 125°C, Unbiased Time = 1000hrs	40	0

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